

<b>Notice of References Cited</b>	Application/Control No. 09/858,437	Applicant(s)/Patent Under Reexamination JOHNSON ET AL.	
	Examiner AHMED ELALLAM	Art Unit 2662	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,642,349	06-1997	Cloonan et al.	370/360
	B	US-5,734,649 A	03-1998	Carvey et al.	370/355
	C	US-5,923,644 A	07-1999	McKeown et al.	370/230
	D	US-6,212,182 B1	04-2001	McKeown, Nicholas W.	370/390
	E	US-6,351,466 B1	02-2002	Prabhakar et al.	370/413
	F	US-6,424,655 B1	07-2002	Horst, Robert W.	370/412
	G	US-6,370,148 B1	04-2002	Calvignac et al.	370/412
	H	US-6,614,796 B1	09-2003	Black et al.	370/403
	I	US-6,625,121 B1	09-2003	Lau et al.	370/230
	J	US-6,633,580 B1	10-2003	T.o slashed.rudbakken et al.	370/461
	K	US-6,735,212 B1	05-2004	Calamvokis, Costas	370/412
	L	US-6,724,779 B1	04-2004	Alleyne et al.	370/517
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Mekkittikul et al, A Practical Scheduling Algorithm to Achieve 100% Troughput in Input-Queued Switches, IEEE 1998, pages 792-799.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.